

Beispielstudienplan Double Degree mit UPV

Studiengang: etit, Vertiefung DT



etit

| TU | | | | | UPV | | |
|-------------------------------------------------------|-----------------------|----|----------------------------|-----------------------------------------------------------------|------------------------------------------------------------------|------------|-----|
| | | CP | Nummer | Titel | Kursname | Nummer | CP |
| Pflicht (26 CP) | Grundlagen | 6 | 18-ho-2010 | Advanced Digital Integrated Circuit Design (V3 + Ü1) | | | |
| | | 6 | 18-sm-2010 | Kommunikationsnetze II (VL3 + Ü1) | | | |
| | | 6 | 18-hb-2030 | Rechnersysteme II (VL3 + Ü1) | | | |
| | | 6 | 18-su-2010 | Software-Engineering - Wartung und Qualitätssicherung (V3 + Ü1) | | | |
| | | 2 | 18-sm-2290 | Industriekolloquium (Ko2) | | | |
| Wahlkataloge (34 CP) | DT I min. 15 CP | 6 | 18-hb-2020 | High-Level Synthese (V3 + Ü1) | | | |
| | | 9 | weitere gemäß Prüfungsplan | | | | |
| | DT II min. 10 CP | 6 | 18-ho-2120 | Advanced Integrated Circuit Design Lab (Pr3) | | | |
| | | 4 | weitere gemäß Prüfungsplan | | | | |
| | | | | | | | |
| | DT III min. 5 CP | 5 | 20-00-0701 | Fortgeschrittener Compilerbau (IV3)Ⓜ | | | |
| | | | weitere gemäß Prüfungsplan | | | | |
| Ingenieur- und Naturwissen- schaften (21 CP) | | | | | Parallel Programming Technology | 33017 | 4 |
| | | | | | Real-time and Embedded Systems | 33449 | 6 |
| | | | | | Hardware Software Codesign | 33453 | 6 |
| | | | | | Transport and Distribution Cable Networks | 33462 | 6 |
| | | | | | | | |
| Studium Generale (9 CP) | | | | | Spanish Language IV General (B) B2 | 13459 | 4,5 |
| | | | | | Technical and Financial Management of Telecommunication Projects | 33438 | 4,5 |
| | | | | | Spanisch avanzado III - Preparación a UNICert II | 41-34-0062 | 3 |
| Master-Thesis | | | | | | 30 | |
| SUMME | | 60 | | 1 Jahr an TU DA belegt | 1 Jahr an UPV belegt | | 60 |
| Masterabschluss | 60 CP + 60 CP= 120 CP | | | | | | |